

## ISE™ 8.1i – Powered by Fmax Technology

	Feature	ISE WebPACK™	ISE Foundation™
Platforms		Microsoft Windows 2000 / XP Red Hat Enterprise Linux 3 (32 bit)	Microsoft Windows 2000 / XP Sun Solaris 2.8 or 2.9 Red Hat Enterprise Linux 3 (32 & 64 bit)
Devices*	Virtex™ Series	<b>Virtex:</b> XCV50 - XCV600 <b>Virtex-E:</b> XCV50E- XCV600E <b>Virtex-II:</b> XC2V40 - XC2V500 <b>Virtex-II Pro:</b> XC2VP2 - XC2VP7 <b>Virtex-4:</b> <b>LX:</b> XC4VLX15, XC4VLX25 <b>SX:</b> XC4VSX25 <b>FX:</b> XC4VFX12	All
	Spartan™ Series	<b>Spartan-II/IIe:</b> All <b>Spartan-3:</b> XC3S50 - XC3S1500 <b>Spartan-3E:</b> All <b>Spartan-3L:</b> XC3S1000L, XC3S1500L	<b>Spartan-II/IIe:</b> All <b>Spartan-3:</b> All <b>Spartan-3E:</b> All <b>Spartan-3L:</b> All
	CoolRunner™ XPLA3 CoolRunner-II CoolRunner-IIA		All
	XC9500 Series		All
Design Entry	Schematic Editor		Yes
	HDL Editor		Yes
	State Diagram Editor		Microsoft Windows only
	Xilinx CORE Generator™ System™		Yes
	RTL & Technology Viewers		Yes
	PACE (Pinout & Area Constraint Editor)		Yes
	Architecture Wizards		Yes
	3rd Party RTL Checker Support		Yes
Xilinx System Generator for DSP		Sold as an Option	
Embedded System Design	Embedded Design Kit (EDK)		Sold as an Option

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Synthesis	XST – Xilinx Synthesis Technology		Yes
	Mentor Graphics Leonardo Spectrum		Integrated Interface (EDIF Interface on Linux)
	Mentor Graphics Precision RTL		Integrated Interface
	Mentor Graphics Precision Physical		EDIF Interface
	Synopsys DC-FPGA Compiler		EDIF Interface
	Synplify/Pro/Premier		Integrated Interface
	Synplicity Amplify Physical Synthesis		EDIF Interface
	ABEL		CPLD (Microsoft Windows only)
Implementation	FloorPlanner		Yes
	PlanAhead™		Sold as an Option
	Timing Driven Place & Route		Yes
	Incremental Design		Yes
	Timing Improvement Wizard		Yes
	Xplorer		Yes
Programming	iMPACT / System ACE™ / CableServer		Yes
Board Level Integration	IBIS, STAMP, and HSPICE** models		Yes
	ELDO Models** (MGT only)		Yes
Verification	ChipScope™ Pro		Sold as an Option
	Graphical Testbench Editor		Microsoft Windows only
	ISE Simulator Lite		Yes
	ISE Simulator		ISE Simulator is available as an Optional Design Tool for ISE Foundation only
	ModelSim® XE III Starter		Yes
	ModelSim XE III		Sold as an Option
	Static Timing Analyzer		Yes
	FPGA Editor with Probe		Yes
	ChipViewer		Yes
	XPower (Power Analysis)		Yes
	3rd Party Equivalency Checking Support		Yes
	SMARTModels for PowerPC™ and RocketIO™		Yes
3rd Party Simulator Support		Yes	

\*For more information on ISE support for Xilinx Aerospace and Defense and Xilinx Automotive FPGAs, visit [www.xilinx.com/ise](http://www.xilinx.com/ise)

\*\*HSPICE and ELDO Models are available at the Xilinx Design Tools Center at [www.xilinx.com/ise](http://www.xilinx.com/ise)  
For more information, visit [www.xilinx.com/ise](http://www.xilinx.com/ise)